

# Synthesis and Electrical Characterization of BaTiO<sub>3</sub> Thin Films on Si(100)

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## ABSTRACT

BaTiO<sub>3</sub> thin film has been deposited on Si(100) substrate using sol-gel process and deposited by using spin – coating technique. The BaTiO<sub>3</sub>/Si(100) structures were studied by structural and electrical characteristics. The X-ray diffraction of BaTiO<sub>3</sub>/Si(100) shows that the diffraction peaks become increasing sharp with increasing calcination temperatures indicating the enhance crystallinity of the films. Scanning electron microscopy of BaTiO<sub>3</sub> thin films shows the crack free and uniform nature. The capacitance-voltage measurement of BaTiO<sub>3</sub> thin film deposited on Si(100) annealed at 600 °C shows large frequency dispersion in the accumulation region. The current-voltage measurement of BaTiO<sub>3</sub>/Si shows the ideality factor was approaches to unity at 600 °C.

**Keywords:** Sol-Gel Technique, BaTiO<sub>3</sub> Thin Film, C-V, I-V

## 1. Introduction

Barium Titanate (BaTiO<sub>3</sub>), a well-known dielectric material has been used as an insulating material to fabricate MIS structures. BaTiO<sub>3</sub> exhibits several advantages, properties such as high charge storage capacity, good insulating property, low leakage current density and high dielectric breakdown strength. It is also a potential material for active microwave tunable device because of its variable dielectric constant under external electric field. It has also been shown that BaTiO<sub>3</sub> works as an excellent buffer layer for YBa<sub>2</sub>Cu<sub>3</sub>O<sub>7-δ</sub> high-T<sub>c</sub> superconductor on various substrates, in particular Si and Al<sub>2</sub>O<sub>3</sub> [1]. Recently, J. S. Lee *et al.* [2] fabricated n-MOSFET structures sequentially with the CMOS process and investigated the insulator characteristics, programming and resistance behaviour of BaTiO<sub>3</sub> films. BaTiO<sub>3</sub> are transparent in the visible and infrared [3] and possess strong electro-optic coefficients making them attractive for active and passive optical components.

BaTiO<sub>3</sub> thin film can be grown using pulsed laser deposition [4], chemical vapor deposition (CVD) [5], metal organic chemical vapour deposition [6-7], polymeric precursor method [8-11], sol-gel synthesis [12] and molecular beam epitaxy (MBE) [1,13]. MBE can be used to grow complex oxide thin film substrate with atomic layer accuracy [14]. High quality BaTiO<sub>3</sub> thin films are gener-

ally grown on lattice matched substrate such as MgO and SrTiO<sub>3</sub>. Integration of BaTiO<sub>3</sub> thin film with silicon processing may enable the properties of ferroelectric to be utilized in combination with CMOS in multi material integration. Ion beam – assisted deposition of various template layers such as MgO and Ytria – stabilized zirconia has been used to develop thin film of super conducting YBa<sub>2</sub>Cu<sub>3</sub>O<sub>7</sub> as well as oriented layers of (Pb, Ba) TiO<sub>3</sub> [15,16].

In recent years, the sol-gel technique has gained interest in the area of processing of thin films because of the several advantages it offers, such as easier composition control, better homogeneity, low processing temperature and low equipment cost [17]. For the present investigations BaTiO<sub>3</sub> thin films have been deposited on Si(100) substrates using sol-gel technique through organic precursor route.

## 2. Experimental Details

(100) oriented Si wafers were well rinsed with warm acetone and methanol followed by etching in HNO<sub>3</sub>/HF (1:1) for 1 minute. BaTiO<sub>3</sub> thin films were deposited on the polish side of the Si using sol-gel process.

Barium acetate (Sigma Aldrich, UK) and Titanium butoxide were used as a starting material. Barium acetate was dissolved in glacial acetate acid and reflux in a reflux condenser at a temperature of about 120°C for six

hours to obtain a clear solution, after obtaining a clear solution, Titanium butoxide was added in a proper mole ratio and the solution was stirred to get homogenous precursor. The viscosity of the solution was varied by the addition of 2-methoxy ethanol. The precursor solution was coated on Si(100) wafers by spin coating technique. The spin coating was done at the rate of 3000 rpm. The film was annealed at 300°C and 600°C for 30 minutes.

The focus of the present work is to study of BaTiO<sub>3</sub> thin films prepared by the sol gel technique and deposited by using spin – coating technique. The BaTiO<sub>3</sub>/Si(100) structures were studied by structural and electrical characteristics. The crystalline nature and the phase formation of the deposited thin film were confirmed by power X-ray diffraction analysis using CuK<sub>α</sub> radiation ( $\alpha = 1.5418$  A.U.) surface morphology of the film were observed using scanning electron microscopy.

The C-V characteristics of the MIS diodes were measured at 1 MHz frequency at room temperature in the dark using a lock in amplifier (EG & G Model 5204). The ohmic contact was made on the back side of Si(100) by successive deposition of Au-Ge (2:1) alloy under a vacuum of 10<sup>-6</sup> mbar and annealed at 400°C under argon atmosphere for 2 minutes.

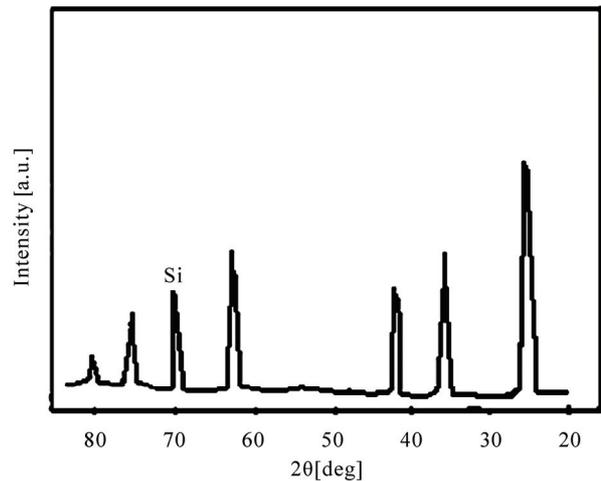
### 3. Results and Discussion

The crystalline quality of BaTiO<sub>3</sub> precursor were prepared by sol-gel technique and deposited by using spin – coating technique. The **Figure 1** shows the XRD pattern of BaTiO<sub>3</sub> thin film on Si(100) substrate with different annealing temperatures. As deposited BaTiO<sub>3</sub> thin film and annealed at 300°C shows amorphous in nature. The crystalline behaviors of the deposited BaTiO<sub>3</sub> thin film annealed at 600°C shows sharp peaks indicating the enhance the crystallinity of the films. The structure of the BaTiO<sub>3</sub> thin film was found to be tetragonal.

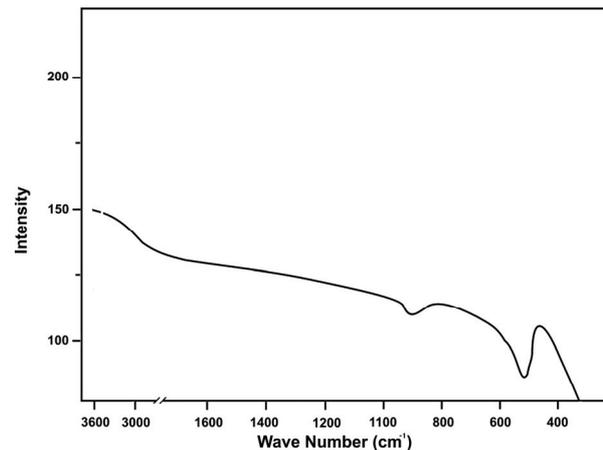
**Figure 2** show the FTIR spectra of BaTiO<sub>3</sub> thin films annealed at 600°C. The intense band at the edge of detection (260 cm<sup>-1</sup>) is assigned to Ti-O mode in developed network [13]. The Figure shows that the broad and strong band observed at 450 cm<sup>-1</sup> characteristics of Ti-O bond.

**Figure 3** (C1 & C2) shows the SEM micrograph of BaTiO<sub>3</sub> thin films annealed at 300°C and 600°C. The BaTiO<sub>3</sub> thin films deposited on Si(100) annealed at 300°C shows the agglomeration of the films shown in **Figure 3** (C1). The BaTiO<sub>3</sub> thin films deposited on Si(100) annealed at 600°C shows the films became crack free and uniform nature.

The electrical properties of BaTiO<sub>3</sub>/Si(100) were studied by using 1 MHz capacitance-voltage characteristics. **Figures 4(a)** and **(b)** shows room temperature C-V characteristics for the sample annealed at 300°C and 600°C of BaTiO<sub>3</sub>/Si(100). **Figure 4(a)** shows that the capacitance



**Figure 1.** XRD pattern of BaTiO<sub>3</sub> thin film on Si(100) substrate annealed at 600°C.



**Figure 2.** FTIR spectra of BaTiO<sub>3</sub> thin films annealed at 600°C.

remains unchanged with in that bias range. **Figure 4(b)** shows BaTiO<sub>3</sub> thin film deposited on Si(100) and annealed at 600°C showed large frequency dispersion in the accumulation region. The capacitance was changed from the inversion value to a value above flat band capacitance at 1 MHz. The electrical properties of BaTiO<sub>3</sub>/Si(100) structure improved with annealing temp. at 600°C shown in **Figure 4(b)**. The result shows that the deposited BaTiO<sub>3</sub> insulator is good enough to use as gate insulator of MIS FET.

For 300°C annealed temperature, the C-V curve shows virtually flat which is an evidence of little modulation of the surface potential by the applied voltage, However when the film is subjected at 600°C annealing temperature the C-V plot shows a dramatic change in the 1 MHz freq. C-V behavior and considerable reduction in the frequency dispersion suggest a much closer approach to

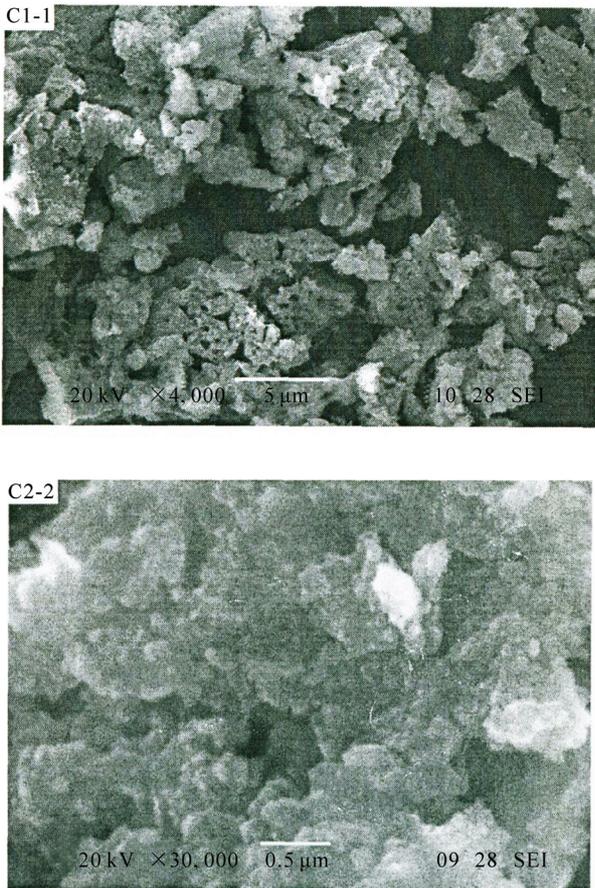


Figure 3. SEM of BaTiO<sub>3</sub> thin films annealed at (C1) 300°C and (C2) 600°C.

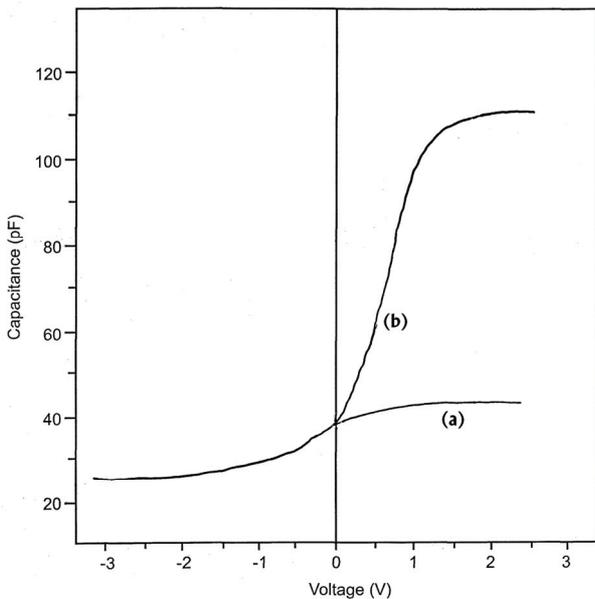


Figure 4. Capacitance-voltage characteristics of BaTiO<sub>3</sub>/Si (100) annealed at (a) 300°C; (b) 600°C.

surface accumulation, *i.e.*, usually observed in the metal insulator semiconductor structure. Hence, the electrical properties of BaTiO<sub>3</sub>/Si(100) MIS structure improved with annealing temperature at 600°C.

Figure 5 shows Current-Voltage (I-V) Characteristics of a typical diode with 90 Å thick BaTiO<sub>3</sub> film on Si (100). The room temp. Current-voltage characteristics were obtained to access the quality of deposited thin films. The forward bias current-voltage characteristics were measured and fitted to the equation

$$J = J_o \left( e^{qv/nKT} - 1 \right) \quad (1)$$

where  $J$  is the current density in amps/cm<sup>2</sup> and  $n$  is ideality factor of the diode. When  $n$  is close to unity, the results of thermionic emission theory are valid such that,

$$J_o = A^{**} T^2 \times e^{-\Phi/KT} \quad (2)$$

where  $A^{**}$  is the modified Richardson constant in amp/cm<sup>2</sup> and  $\Phi$  – Barrier height.

The saturation current density  $J_s$  is given by

$$J_s = A^* T^2 \exp\left(-\frac{q\phi Bn}{kT}\right) \quad (4)$$

The parameter  $n$  is given by

$$n = \left[ 1 + \frac{\partial \Delta\phi}{\partial V} + \frac{kT}{q} \frac{\partial (\ln A^*)}{\partial V} \right]^{-1} \quad (5)$$

where  $\phi$  is the height of Schottky barrier,  $\Delta\phi$  is the Schottky barrier lowering and  $A^*$  is the effective Richardson constant.

For the annealing temperature at 300 and 600°C of BaTiO<sub>3</sub>/Si shows the ideality factor was found to be varying

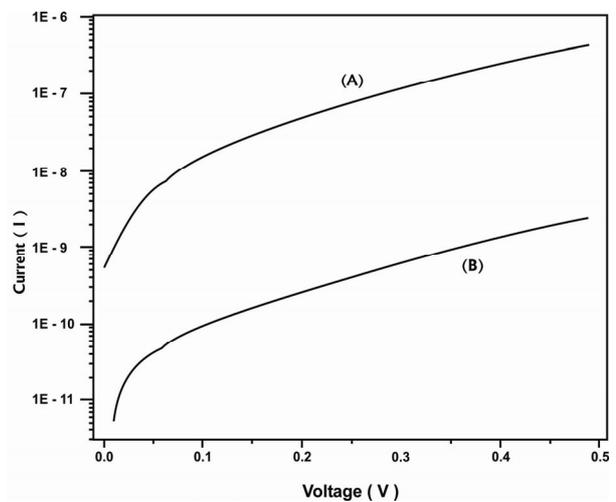


Figure 5. I – V Characteristics of a Typical diode with 90 Å thick BaTiO<sub>3</sub> film on Si (100) with annealed at (A) 300°C and (B) 600°C.

between 1.2 to 1.1. The value of  $n = 1$  is consistent with current injection through a shottkey barrier, While large value indicates conduction by generation recombination. A Poor ideality factor can have variety of interpretation among them, the effect of Tunneling can be excluded because of low carrier density in substrates. The ideality factor decreases with increasing annealing temp. of BaTiO<sub>3</sub>/Si as shown in **Figure 5**.

These results show that the electrical properties of BaTiO<sub>3</sub>/Si, MIS structure improved at higher annealing temp.

#### 4. Conclusions

BaTiO<sub>3</sub> thin film has been synthesized using sol-gel process and deposited on Si (100) by using spin – coating technique. The BaTiO<sub>3</sub>/Si(100) structures were studied by structural and electrical characteristics. The XRD and SEM of BaTiO<sub>3</sub>/Si(100) indicates the enhance crystallinity of the films with annealing temperature at 600°C. The C-V measurement of BaTiO<sub>3</sub> thin film deposited on Si(100) annealed at 600°C shows large frequency dispersion in the accumulation region. The current-voltage measurement of BaTiO<sub>3</sub>/Si shows the ideality factor was approaches to unity at 600°C.

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