Investigation and Implementation of a PIC-Based Sensor Node for Wireless Sensor Networks

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Abstract

The basic component of wireless sensor networks is sensor node, and it is one of the most investigated complex issues in wireless sensor networks. Within this investigation concept, one of the major topics focused on wireless sensor networks is designing sensor node equipment. In this study, a new wireless sensor node design was proposed. In this design, a low-power PIC and a low-power transceiver-receiver device were used. In order to connect various sensors to the node, an expansion slot was designed. The software that runs on the microcontroller was written in the PIC-C language, and it could be adapted for different routing algorithms and sensor types. So this paper presents an alternative low cost and low powered sensor node design and implementation for usage of the researchers and practitioners.

Keywords

Sensor; Wireless Sensor Networks; Microcontrollers; RF Module

1. Introduction

In recent years, development of Micro-electro-mechanical systems (MEMS), wireless communication technologies and digital electronic developments and improvements of these technologies bring out the production and funding of low-cost, low-power demanding multifunctional sensor nodes. A small mobile phone or a coin sized these sensor nodes can communicate with each other or a base station. Ability of sensing, data processing, wireless communications, data storage and interoperability specifications have revealed the idea of wireless sensor networks [1]. Figure 1 shows a sample wireless sensor network structure and additional equipments of the structure. The most distinctive feature of the traditional wireless sensor networks is the ability of working together. The data obtained by sensors can be shared between themselves and can be transferred to each other. The feature of transmitting the data over each other allow low-power sensor nodes to transfer data in an environment where many sensors exist [2].

In previous researches, Lo and Yang designed a sensor node for wireless sensor networks. In their design,
they used Texas Instruments MSP430 microcontroller. MSP 430 has 12 bit ADC, 2 Kb RAM, 60 Kb flash memory. This microcontroller needs 15 µW energy in sleep mode. They used Chipcon CC2420 wireless transceiver for communication. This sensor node has a 512 Kb external memory [8]. Lee and Huang designed a sensor node for wireless sensor networks namely ITRI ZBnode. In their design, they used ARM 720 T microcontroller. ARM 720 T has 10 bit ADC. In this design, an external 16 M RAM and an external 16 M flash memory used. They used Chipcon CC2420 wireless transceiver for communication. ZBnode can be powered with an AC adapter or a Li-ion battery with a DC input range from 3.5 to 5.5 V [9]. Körber et al. designed a PIC 18F452 based sensor node. PIC 18F452 has 32 Kb flash memory and 1.5 Kb RAM. In this sensor node, they used TINYOS Operating System. They used TCM 120 wireless transceiver for communication [10].

In this study an alternative low cost, low powered sensor node was designed [11]. The rest of the paper organized as follows. Section 2 presents the hardware components of the designed sensor node. Section 3 describes the software components of the designed sensor node. Section 4 concludes the paper.

2. PIC-Based Sensor Node Platform

The proposed system was composed of a PIC microcontroller, wireless transceiver modules, LED indicators and a power regulator. Sensor or sensors could connect to the platform via the expansion slot. For examining the system, a sht11 humidity sensor and a temperature sensor were connected to the system and then the system evaluation procedure was carried out. Besides, both software and hardware structures of the system supports other types of sensors. Figure 2 shows an image of the designed system.

Figure 1. Wireless sensor network.

Figure 2. PIC-based sensor node.
2.1. Hardware

The sensor node uses a PIC chip which is Microchip 18F4620. This microcontroller has a 16-bit processor, supports 40 MHz external oscillator, has 36 input-output, 64 K Flash, 4 K RAM, 1 K EEPROM, 13-bit analog to digital converter (ADC), 3 timers, pulse-width modulation (PWM), 1 EUSART (Enhanced Universal Synchronous Asynchronous Receiver Transmitter) features [3]. In order to reduce the power consumption, no external memory unit was used. Udea UFM-M11 was used as wireless transceiver unit. UFM-M11 works in 434 Mhz band and can run on 10 different channels, and it can also operate at the speed of 9.6 Kbps [4]. It operates 2.7 - 3.3 V DC range. UFM-M11 was connected to the serial port of the microcontroller. Figure 3 shows the modular view of the designed sensor node.

The detailed circuit drawing of the equipment, and the links in the designed system was shown in Figure 4. J15 port in circuit diagram indicates expansion slot for the external sensor or sensors.

2.2. Power Management

The power unit which provides power to the sensor node unit was shown in Figure 5. 18F4620 microcontroller can operate within the range of 2.0 - 5.5 V. UFM-M11 wireless transceiver unit can also operate within the range 2.7 - 3.3 V. So the system works on 3 V because both UFM-M11 and microcontroller support this voltage.

In Table 1, the power consumption of the design was compared with the sensor nodes on the market. The consumption data was measured while the system is running and reflects the maximum amount of consumption. When operating at the maximum power, the power consumption would be high and the battery life would be shorter. However, with the help of low-power routing algorithms and media access (MAC) protocols, consumption would decrease and the battery life would be longer.

2.3. Medium Access Control (MAC)

During communication, media access protocol (MAC) determines the interaction with devices. MAC protocol for wireless sensor networks has a special significance. The power consumption can be reduced due to the MAC protocol so the sensor nodes with limited storage facilities, can use their memories better.

2.3.1. Timeout MAC (T-MAC)

This protocol is another derivative of the S-MAC protocol. In this protocol there is no regular sleep-wake cycle. Sleeping operation takes place when the sensor node is unable to obtain data in a certain time. Waking can start with start of sending data or when the sleeping time expires. Sensor nodes have a certain time limit for sleeping. The sample demonstration could be given as in Figure 6.

2.3.2. Traffic Adaptive Medium Access Protocol (TRAMA)

In this protocol, sensors become synchronized by waiting each other. When the media is empty, the sensors have access to the environment [7].

2.3.3. T-MAC and TRAMA Hybrid MAC

In this study, a hybrid MAC protocol was developed utilizing the T-MAC and TRAMA protocols. The proposed

![Figure 3. Sensor node’s modular view.](image-url)
Figure 4. Hardware circuit design and links.
Figure 5. Regulator.

Table 1. Comparative data on power consumption.

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Microcontroller</td>
<td>PIC 18F452</td>
<td>ATMEL 128 L</td>
<td>TI MSP430</td>
<td>PIC 18F4620</td>
</tr>
<tr>
<td>Active Mod (mA)</td>
<td>13.40</td>
<td>8.90</td>
<td>1.70</td>
<td>10</td>
</tr>
<tr>
<td>Sleep (µA)</td>
<td>8.00</td>
<td>27.70</td>
<td>3.30</td>
<td>3.5</td>
</tr>
<tr>
<td>Tx (mA)</td>
<td>9.90</td>
<td>15.50</td>
<td>19.40</td>
<td>40</td>
</tr>
<tr>
<td>Rx (mA)</td>
<td>15.80</td>
<td>10.70</td>
<td>21.10</td>
<td>13</td>
</tr>
<tr>
<td>Input (V)</td>
<td>4.75</td>
<td>2.70</td>
<td>1.80</td>
<td>3</td>
</tr>
<tr>
<td>Data Rate (kbps)</td>
<td>120.00</td>
<td>38.40</td>
<td>250.00</td>
<td>9.6</td>
</tr>
<tr>
<td>Tx Output Power (dBm)</td>
<td>10.00</td>
<td>5.00</td>
<td>0.00</td>
<td>10.00</td>
</tr>
<tr>
<td>Tx Input /Tx Output</td>
<td>4.70</td>
<td>13.23</td>
<td>34.92</td>
<td>5.00</td>
</tr>
<tr>
<td>Rx sensitivity (dBm)</td>
<td>−95.00</td>
<td>−98.00</td>
<td>−94.00</td>
<td>−107.00</td>
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</table>

Figure 6. T-MAC protocol [6].

hybrid protocol was obtained by making changes on the T-MAC protocol. When the sensor node wakes up, it does not start to send data, instead, it just waits until discharging of the medium. So the sensor node can adapt itself to the traffic and it could also be possible to sleep sometimes then wake another times.

2.4. Routing Algorithm

Routing algorithm is one of the complex problems in wireless sensor networks. Routing algorithm determines
the way the data will go through. Many proposals have been made to solve this problem. In this study flooding algorithm was used. According to this algorithm, the data obtained with the sensing node is sent to other sensors through the border nodes as if the water flows. Illustrative demonstration of this process could be seen in Figure 7.

3. Software

3.1. Microcontroller Software

All operations of the sensor nodes are managed by the software. To fulfill this task, the operating system had been developed using the PIC-C language. The sensor node operating system was responsible to determine the routing algorithm and the MAC protocol, sending data retrieval, data processing, reading data from analog and digital sensors and memory management etc. Routing algorithm and MAC part of the Microcontroller software has been discussed above. For wireless communication microcontroller software uses the packet structure as shown in Table 2.

The microcontroller’s memory has capacity to store 1000 packets. The reason to store that much packet was that if the network gets busy, the data should wait in the microcontroller’s memory. Figure 8 illustrates the hierarchy in the data preparation and standby and Table 3 shows the memory information of running software on the microcontroller.

![Figure 7. Flooding algorithm.](image)

Table 2. The data packet structure.

<table>
<thead>
<tr>
<th>Sensor ID</th>
<th>Data ID</th>
<th>Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 byte</td>
<td>1 byte</td>
<td>1 byte</td>
</tr>
</tbody>
</table>
Figure 8. Transferring the data to the sensors queue.

Table 3. The area covered by the program memory of the microcontroller.

<table>
<thead>
<tr>
<th></th>
<th>Used</th>
<th>Total</th>
<th>%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Program memory</td>
<td>C35h(3125)</td>
<td>10000h byte</td>
<td>4.8%</td>
</tr>
<tr>
<td>Data memory</td>
<td>C16h(3094)</td>
<td>F80h byte</td>
<td>78.0%</td>
</tr>
<tr>
<td>EEPROM</td>
<td>0h</td>
<td>400h byte</td>
<td>0.0%</td>
</tr>
</tbody>
</table>

3.2. Monitoring Software

The data obtained from the wireless sensor network were stored in certain centers. Through these centers, the obtained data could be observed and processed. Observation software had been developed for the sensor nodes. With the developed software, the data could be observed and stored. Figure 9 shows the interface of this software.

The communication between the computer the observation software and wireless sensor node was carried out through USB. FT232 USB-UART interface integrated circuit was used for USB communication. Sensor nodes connected to the network which appear in monitoring software could be shown in Figure 10.

The data collected from the network was stored for processing and displaying later. Figure 11 demonstrates the recorded data.

In Table 4 the comparison data provided with the designed sensor node and the other sensor nodes are shown.

4. Conclusion

There are several studies utilized to solve the problems of wireless sensor networks. Several sensor nodes were designed and implemented. Different needs have led to the design of different sensor nodes. To solve some of these problems, there is a need for designing smaller sized sensor node. In some cases, the maximum battery life becomes the first aim. In some applications, more memory space is needed. In some cases, more cost-effective hardware and software are needed.
Figure 9. Sensor monitoring software.

Figure 10. Image sensors are connected to the network application.

Figure 11. Recorded data.
Table 4. Feature comparison of sensor nodes.

<table>
<thead>
<tr>
<th></th>
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<th></th>
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<th></th>
<th></th>
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</thead>
<tbody>
<tr>
<td>Microcontroller</td>
<td>PIC 18F4620</td>
<td>Atmega128l</td>
<td>Atmega128l</td>
<td>MSP430F</td>
<td>ARM7</td>
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<tr>
<td>Architecture</td>
<td>16-Bit</td>
<td>8-Bit</td>
<td>8-Bit</td>
<td>16-Bit</td>
<td>32-Bit</td>
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<tr>
<td>Speed</td>
<td>40 MHz</td>
<td>7.3728 MHz</td>
<td>4 MHz</td>
<td>8 MHz</td>
<td>12 MHz</td>
</tr>
<tr>
<td>Program memory</td>
<td>64 KB</td>
<td>128 KB</td>
<td>128 KB</td>
<td>48 KB</td>
<td>512 KB</td>
</tr>
<tr>
<td>Data memory</td>
<td>1 KB</td>
<td>4 KB</td>
<td>4 KB</td>
<td>10 KB</td>
<td>11 KB</td>
</tr>
<tr>
<td>Storage memory</td>
<td>4 SRAM</td>
<td>512 KB</td>
<td>512 KB</td>
<td>1024 KB</td>
<td>-</td>
</tr>
<tr>
<td>IO</td>
<td>36</td>
<td>51</td>
<td>18</td>
<td>16</td>
<td>30</td>
</tr>
<tr>
<td>Built-In sensors</td>
<td>2</td>
<td>2</td>
<td>2</td>
<td>5</td>
<td>-</td>
</tr>
<tr>
<td>Interface</td>
<td>3 LED</td>
<td>3 LED</td>
<td>1 LED</td>
<td>3 LED 1 Buton</td>
<td>1 LED</td>
</tr>
</tbody>
</table>

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References


