A/D Restrictions (Errors) in Ultra-Wideband Impulse Radios

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Abstract

Ultra-Wideband Impulse Radio (UWB-IR) technologies, although are relatively easy in transmission but they present difficulties in reception, in fact the reception of such waveform is a quite complicated matter. The main reason is that in fully digital receiver the received waveform must be sampled at a rate of several GHz. This paper focuses on the impact of the Analog to Digital (A/D) conversion stage that is used to sample the received waveform. More specifically we focus on the impact of the two main parameters that affect the performance of the Software Defined Radio (SDR) system. These parameters are the bit resolution and the time jittering. The influence of these parameters is deeply examined.

Keywords: UWB, Impulse Radio, ADC, Jitter Error, Quantization Error

1. Introduction

UWB transmission has recently received great attention in academia and industry for applications in wireless communications. A UWB system is defined as any radio system that has a 10 dB fractional bandwidth larger than 20% of its center frequency, or has a 10 dB bandwidth larger than 500 MHz. It is expected that many approaches used for short-range wireless communications will be revaluated and a new industrial sector with high data rate will be formed. Fully digital receiver for UWB-IR requires the use of A/D conversion and SDR techniques as described below. The RF waveform received from the antenna is directly digitized from the antenna via an A/D conversion stage. Then the digital information derived from the UWB waveform is handled and processed by a DSP. However, this process, introduce new signal distortions, due to the new uncertainties introduced, that are the jitter error and the quantization error. The latter comes exclusively from the bit resolution of the A/D converter, while time jittering comes merely from the aperture jitter of the ADC, and from clock jitter of the sampling circuitry [1,2]. In this paper we examine the impact of those two parameters on the bit error rate performance of an UWB-IR fully digital receiver. In UWB-IR systems a pulse train, consisting of very short pulses and occupying very large spectrum [3], is transmitted. Several modulation schemes are used such as Bi-phase, Pulse Position, On-Off keying etc. [4]. In this paper we choose Binary Pulse Position Modulation (BPPM). We consider transmission through indoor multipath environment [5], in the presence of white Gaussian noise. The performance of the system is evaluated by the bit error probability (BEP) in terms of jitter and quantization noise. An expression of BEP is derived and numerically results are presented.

2. Analog to Digital Conversion

During the A/D conversion additional noise is produced at the output of the A/D converter due to two main reasons: Quantization and Jitter error. The first is illustrated in Figure 1 (a) and it is a result of the difference between the analog, continuous input signal and the digitized output of the ADC. The finite ADC resolution gives the form of the stairs-like signal. If an ADC has a bit resolution of $N$ bits, it means that the output signal is coded at $2^N$ different binary numbers, from 0 to $2^N-1$. Let assume that the input signals peak to peak amplitude ($V_{pp}$) is the same with the ADC full-scale voltage range. Then the corresponding quantization step is $Q = V_{pp} / 2^N$. An amplitude value at the input is mapped to the nearest $N$ bit binary number and the
absolute difference between input-output can be from zero to $Q/2$, thus the quantization error is from $-Q/2$ to $Q/2$. We assume that the input signal can take any random value within a quantization step, with equal probability. Therefore the distribution of quantization error is uniform and its probability density function $f(x)$, is shown in Figure 1(b). Obviously, it has a mean of zero and it is easy to prove that the standard deviation of quantization error is $\sigma_q = Q/\sqrt{12}$, as follows,

$$\sigma_q^2 = \int_{-\infty}^{\infty} x^2 f(x) dx = \frac{1}{Q} \int_{-\frac{Q}{2}}^{\frac{Q}{2}} \frac{x^2}{\frac{Q}{2}} dx = \frac{2}{Q} \frac{Q^3}{24} = \frac{Q^2}{12}$$

The second error that concerns our study is called jitter error and it is a result of the non infinite timing precision of the sampling procedure and the ADC imperfections. The fact is that there is an uncertainty at the sampling time which causes an uncertainty at the input voltage of the signal. This effect is shown in Figure 2. Let the input signal at an A/D converter be $V(t)$. We focus at the time $t_j$, corresponding at a multiplicative of sampling period. Due to jitter effect the sample taken by the ADC is the one at the time $t_i + \Delta t_j$, where $\Delta t_j$ is a random variable, assuming normally distributed with zero mean and standard deviation $\sigma_j$. The corresponding voltage error is then, $\Delta V_j = V(t_j + \Delta t_j) - V(t_j)$. By rewriting this expression we have,

$$\Delta V_j = V(t_i + \Delta t_j) - V(t_i) = \left[ \frac{V(t_j + \Delta t_j) - V(t_j)}{\Delta t_j} \right] \Delta t_j$$

For small $\Delta t_j$ we can approximate the expression in the brackets with the first derivative of $V(t)$ [6], and obtaining,

$$\Delta V_j \approx \left. \frac{dV(t)}{dt} \right|_{t=t_j} \Delta t_j = \Delta t_j V'(t_j) \tag{2}$$

3. Signal Model Description

The transmitted pulses have the form of a Gauss monocycle, i.e. the first derivative of a standard Gauss pulse. Figure 3 shows a schematic representation of the BPPM modulated transmitted signal. The bit period is $T_f$ (frame period) and the time offset $\Delta$ represents the modulation index. Time is divided into frames, the period of

![Figure 1. (a) Digitization of an analog continuous signal (dotted line) to discrete and quantized signal (normal line); (b) Uniform distribution $f(x)$ of quantization error $x$.](image1)

![Figure 2. Jitter error effect.](image2)

![Figure 3. BPPM signaling.](image3)
the frames is $T_f$. We determine the symbol by its position within each frame. A logic “0” is a pulse at the beginning of the frame, while a logic ‘1’ is delayed by a small amount of time $\Delta$. The modulated pulses waveform $s(t)$ is expressed by Equation 3 below,

$$s(t) = \sum_{j=0}^{N-1} \sqrt{E_b} w(t - jT_f - b_j \Delta)$$

(3)

where, $w(t)$ is the pulse shape (first derivative of a Gaussian pulse) normalized to have total energy $\int_{-\infty}^{\infty} |w(t)|^2 dt = 1$, $E_b$ is the energy per bit, $T_f$ is the frame period, $b_j$ is the j-th bit, $\Delta$ is the BPPM modulation index, $N$ is the total number of modulated pulses. $\sigma_w$ is related to the pulse width with the relationship $T_p = 2\pi \sigma_w$, where $T_p$ represents the width of the pulse. The modulation index $\Delta$ is chosen to satisfy the orthogonality of the transmitted symbols, i.e., $\int_{-\infty}^{\infty} w(t) w(t - \Delta) dt = 0$. We choose $\Delta$ greater than the pulse duration, i.e., $\Delta > T_p$.

4. Theoretical Analysis of Error Probability

In order to derive an expression for the probability of error, we consider the transmit and receive system model shown in Figure 4. The transmitted signal, $s(t)$, described above, propagates through a multipath channel with impulse response $h(t)$. Then it is converted from analog to digital using an A/D converter. As mentioned above the input signal is sampled at the ADC frequency and quantized with corresponding ADC resolution. For the detection of the symbols, a matched filtering technique is used. The matched filter is constructed by two correlators. The received signal is correlated with the expected symbols and the output is the difference of those. The output is sampled every frame period.

We assume perfect channel estimation and synchronization. From this point, the analysis continues for the first frame period, i.e., $0 \leq t \leq T_f$. We use vector notation, which represents the sampled versions of the signals.

![Figure 4. System transmission-reception model.](image)

All the vectors has length, $N_f = T_f \cdot f_s$, where $T_f$ is the frame period and $f_s$ is the sampling frequency. The templates for the two symbols $(x_0, x_1)$ are the transmitted symbols for ‘0’ and ‘1’ respectively after the channel, $n$ is Gaussian process, representing total additive noise, with a mean value of zero and a double side power spectral density, $N_0/2$.

The channel impulse response, corresponding to the IEEE 802.15.3a model [5] for indoor multipath environments, is given by $h(t) = \sum_{l=0}^{L} a_{l} \delta(t - \tau_{l})$, where $L$ is the total resolvable channel paths, $a_{l}, \tau_{l}$ are the gain coefficient and time delay, respectively, for the corresponding $l$ path. Thus the transmitted signal after the channel is expressed as follows,

$$x(t) = (s * h)(t) = \sum_{l=0}^{L} a_{l} s(t - \tau_{l})$$

(4)

where (*) denotes convolution.

The received discrete signal $r$ is given in Equation 5 below,

$$r = x + n + n_{j} + n_{q}$$

(5)

where, $n - N(0, \sigma_{n}^2)$, $\sigma_{n}^2 = N_0/2$ is the total additive noise at the receiver, $n_{j}$ is the noise vector due to jitter error and by using Equation 2, we have: $n_{j} - N(0, \sigma_{j}^2)$, $\sigma_{j}^2 = \sigma_{w}^2 \delta^2$ and $n_{q}$ is the noise term due to quantization noise i.e. $n_{q} - U(\sigma_{q}^2/2)$, $\sigma_{q} = \sigma_{w} \sqrt{12}$.

The derivative $\dot{x}$ is calculated from Equation 3 and Equation 4, as follows,

$$\frac{d}{dt} x(t) = \frac{d}{dt} \sum_{j=0}^{N-1} a_j s(t - \tau_j)$$

$$= \sum_{j=0}^{N-1} \sqrt{E_b} \frac{d}{dt} w(t - jT_f - b_j \Delta - \tau_j)$$

(6)

and by taking the discrete (sampled) vector. The waveform $w(t)$ as mentioned before is the first derivative of a gauss monopulse, thus $\frac{d}{dt} w(t)$ is the second derivative of the pulse. To obtain an expression for the probability of error on symbol detection, we must first define the decision metric at the output of the correlators in Figure 4 at time $t = T_f$. For simplicity we consider the transmission of a ‘0’ and in the same way we can derive an expression for the ‘1’. The decision metric is then,

$$D_0 = r^T (x_0 - x_1) = (x_0 + n + n_{j} + n_{q})^T (x_0 - x_1)$$

$$= x_0^T (x_0 - x_1) + (n + n_{j} + n_{q})^T (x_0 - x_1)$$

$$= x_0^T x_0 - x_0^T x_1 + (n + n_{j} + n_{q})^T (x_0 - x_1)$$

$$= R_{xx}(0) + (n + n_{j})^T (x_0 - x_1) + n_{q}^T (x_0 - x_1)$$
and we obtain,
\[ D_0 = R_x(0) - R_x(\Delta) + N_e + N_q \quad (7) \]
where, \( R_x(\tau) \) is the autocorrelation function of vector \( x \) at time \( \tau \), \( N_e = (n + n_i) \) is a gaussian random process including thermal noise and jitter noise i.e., \( N_e \sim N(0, \sigma^2_{g1}) \), \( \sigma^2_{g1} = (\sigma^2_n + \sigma^2_{\mu}) (x_0 - x_1)^T (x_0 - x_1) \), and \( N_q = n_q^T (x_0 - x_1) \) is the noise term due to quantization which is a summation of \( N_f \) terms of uniformly distributed random variables. Because of the fact that \( N_f \) is usually a sufficiently large number we may use the central limit theorem [7], and approximate this term with a Gaussian process with variance \( \sigma^2_{g2} \) i.e.,
\[ N_q \sim N(0, \sigma^2_{g2}) \], \( \sigma^2_{g2} = \sigma_q^2 (x_0 - x_1)^T (x_0 - x_1) \)

Therefore the decision metric is a Gaussian r.v. with mean \( R_x(0) - R_x(\Delta) \) and standard deviation \( \sqrt{\sigma^2_{g1} + \sigma^2_{g2}} \), thus the probability of error is expressed as follows:
\[ P_e = Q \left( \frac{R_x(0) - R_x(\Delta)}{\sqrt{\sigma^2_{g1} + \sigma^2_{g2}}} \right) \quad (8) \]

5. Numerical Results

After the above analysis we calculate the error probability numerically using simulation program to evaluate Equation 8 and by averaging over 1000 channel realizations corresponding to IEEE 802.15.3a model CM1. The parameters that used are: width of the pulses \( T_p = 200 \) psec, modulation index \( \Delta = 1 \) nsec, frame period \( T_f = 100 \) nsec, sampling frequency \( f_s = 20 \) GHz, yielding a channel time resolution of 50 psec. Figure 5 shows the bit error probability (BEP) as a function of signal to noise ratio with 6 bit ADC resolution and with different number of jitter standard deviation. We can see that jitter is a significant factor to the performance especially when noise has lower power. Beyond 10 dB of signal to noise ratio, jitter is the main cause of performance degradation.

Figure 6 shows the error probability as a function of jitter standard deviation. On top of the graph we set the bit resolution of A/D at 4 bits and the curves correspond to several signal to noise ratios. Again we can see that in cases of higher SNR, the error probability has strong dependence on jitter. In the graph at the bottom, we set SNR to 10 dB and we change the bit resolution of ADC. It is interesting to notice that an increase of bit resolution more than 4 bits doesn’t improve performance. The dependence of error probability of bit resolution is shown

![Figure 5](https://example.com/figure5.png)

**Figure 5.** Bit error probability as a function of signal to noise ratio (Eb/N0) for different values of jitter standard deviation (psec), with 6 bit ADC resolution

![Figure 6](https://example.com/figure6.png)

**Figure 6.** Bit error probability as a function of jitter standard deviation (psec), varying signal to noise ratio, with 4 bit ADC resolution (top graph) and varying ADC resolution with Eb/N0=10dB (bottom graph).
in Figure 7, with jitter standard deviation at 1 psec and varying SNR. In all cases there is a limit at bit resolution and it is obvious that a use of 4 bits is adequate to lead to a sufficient performance.

6. Conclusions

In the present paper we have studied the impact of the two parameters that affect the performance of the digitizing stage. These parameters are the jitter error and the quantization error. The error probability dependence from both parameters was investigated and presented. Both of them are critical to error performance of Ultra-Wideband Impulse Radio systems. Jitter error plays an important role especially when additive noise is not very strong. Quantization error is also a significant factor for the BEP improvement for bit resolution below 4 bits. For more than 4 bits of ADC resolution the improvement is negligible. From the above study in order to assure low BEP, the jitter must be kept as low as possible (2-3 psec) and the ADC resolution above 4 bits.

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8. References


