Distributed Coding Modulation Adaptation Scheme for Relay Channel

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ABSTRACT

Rate adaptation is an effective approach to achieve high spectrum efficiency under varying channel condition, especially for wireless communication. This paper proposes rate adaptation at receiver for wireless relay system. In this scheme, source node uses a new modulation technology, called random projections code (RPC), to achieve rate adaptation. Both relay node and destination node decode the received RPC encoding signals. If destination does not decode RPC correctly, relay node will act compressing and forwarding role by performing LDPC syndrome encoding and sending syndrome coded information to destination node. We discuss how to jointly decode at destination node when it receives RPC coded information from source node and syndrome coded information from relay node. Finally, we evaluate the scheme by bit-error-rate (BER) and good put evaluation metrics. Simulation results show that the coding gain is about 4 dB, 3.1 dB, 2.2 dB and 1.6 dB for LDPC coding rate 0.8, 0.89, 0.94, 0.99 at BER 10⁻⁵ respectively. The throughput of the schemes is at least 0.3 bit/s/Hz higher than RPC at SNR ranging from 5 dB to 25 dB.

Keywords: Rate Adaptation; Random Projection Code; LDPC; Relay Channel

1. Introduction

Rate adaptation is critical to the performance of modern wireless communication system, e.g., WiFi, 3G and LTE. Conventional rate adaptation technology is implemented at sending end, and achieved by adjusting actual transmission rate through channel coding rate and modulation scheme[1,2], based on the current channel state information (CSI) [3]. However the existing approaches for rate adaptation at sending end have two defects. One is that it is always difficult to estimate CSI accurately due to feedback delay in frequency division duplex (FDD) system, because the channel may vary drastically during several data packets transmission. The other is that the transmission rate can be adjusted among limited modulation coding schemes (MCS), so the coarse granularity of MCS results in a staircase of spectrum efficiency.

To solve these problems mentioned above, three new schemes [4-6] have been proposed for smooth rate adaptation, and all of them implement rate adaptation at receiver and attain continuous spectrum efficiency. Cui et al. [4] has proposed the random projection code (RPC), which is a novel rate compatible modulation (RCM) technology, and modulated symbols are incrementally generated from information bits through weighted mapping. Compared to LDPC [7, 8], probability convolution operation is used in RPC for horizontal iteration instead of log(tan h) operation. This is due to the fact that RPC symbols are generated using the arithmetic weighted sum instead of logical exclusive-OR (XOR). RPC combines channel coding and modulation together. In addition, Reference [4] has mentioned that a high coding-rate LDPC code serially concatenated with RPC can gain better performance, but no detailed performances are given.

On the other hand, how to use RPC in relay channel is a relative new topic. For traditional relay system, some researchers have proposed distributed channel coding (DCC) schemes for the three node relay channel to jointly optimize coding design at source node and relay node. These DCC schemes include distributed turbo codes (DTC) and distributed LDPC codes (DLDPC) [9]. References [10-14] have discussed how to design LDPC code for DF relay scheme. If the signal from relay node to destination node is compressed, those DLDPC models can be thought as typical distributed source coding (DSC). There are two major kinds of approach, i.e., parity approach and syndrome approach, to realize DSC [15,16]. Some researchers have proved that under ideal channel condition syndrome approach has the optimality, but under the noisy channel condition parity approach’s performance is the best [16,17].
Inspired by DCC, DSC and potential coding gain brought by serially concatenating LDPC with RPC, we propose concatenating LDPC with RPC in parallel, and apply this parallel concatenation scheme to relay system. This scheme also has implemented rate adaptation at receiver with the help of LDPC coding at relay node. To our knowledge, the parallel concatenation of LDPC and RPC, and applying it to rate adaptation in relay system are seldom seen in publication.

The relay system consists of three nodes, i.e., one source, one relay and one destination. In our scheme, source node doesn’t need CSI, except the ACK signal from destination node. Relay node and destination node achieve rate adaptation by using RPC. Relay node uses syndrome encoder to generate syndrome bits which are modulated by BPSK, and then transmits the modulation symbols to destination node. In destination node, we adopt joint decoder which needs two input data, i.e., the received RPC encoded information from source node and received syndrome information from relay node. Compared to pure RPC scheme, simulation results show that the coding gain is about 4 dB, 3.1 dB, 2.2 dB and 1.6 dB for coding rate 0.8, 0.89, 0.94 and 0.99 at BER 10^{-5}, the proposed schemes can enhance channel capacity at least 0.3 bit/s/Hz at signal-to-noise ratio (SNR) ranging from 5 dB to 25 dB. The simulation also indicates that the gain is insensitive to distance of relay node to source node and destination node.

The structure of this paper is organized as follows. Section 2 gives the system model of our relay scheme. Section 3 presents the processing procedure of source node and relay node. Section 4 presents how to decode jointly at destination node. The simulation results of our relay schemes are shown in section 5. Finally, Section 6 concludes this paper.

2. System Model

Our system model is depicted as Figure 1. We can see that this system includes three nodes, i.e., source node denoted as S, relay node denoted as R, and destination node denoted as D. There exists three channel links, i.e., SD link, SR link and RD link. The distance between the source and the destination is denoted by $d_{SD}$, $d_{SR}$ standing for the distance between the source and the relay, and $d_{RD}$ standing for the distance between the relay and the destination. Those distances satisfy the condition, i.e., $d_{SD} > d_{SR}$ and $d_{SD} > d_{RD}$.

We assume that relay node operates in a half-duplex mode. AWGN channel model is used for all the links, denoted as $N_{SR}$, $N_{RD}$ and $N_{SD}$ for the SR, RD and SD links, and their noise powers are $\sigma_{SR}^2$, $\sigma_{RD}^2$ and $\sigma_{SD}^2$, respectively. Setting radio signal propagation attenuation factor as $\alpha$, the relationships between the noise powers of different links can be given by

$$\frac{\sigma_{SR}^2}{\sigma_{SD}^2} = \left(\frac{d_{SR}}{d_{SD}}\right)^\alpha \quad \text{and} \quad \frac{\sigma_{RD}^2}{\sigma_{SD}^2} = \left(\frac{d_{RD}}{d_{SD}}\right)^\alpha$$

Let $\alpha = 2$ in this paper. Alternatively, the SNR of the different links are given by

$$SNR_{SR} = \frac{1}{\sigma_{SR}^2} \quad (2a)$$

$$SNR_{SD} = \frac{1}{\sigma_{SD}^2} = SNR_{SR} \left(\frac{d_{SD}}{d_{SR}}\right)^\alpha \quad (2b)$$

$$SNR_{RD} = \frac{1}{\sigma_{RD}^2} = SNR_{SR} \left(\frac{d_{SD}}{d_{RD}}\right)^\alpha \quad (2c)$$

S uses RPC to encode $b$, and each two consecutive symbols are composed one modulation signal denoted as $X_s$. Then S broadcasts $X_s$ to $R$ and $D$. The relay node $R$ uses RPC decoder2 to demodulate the signal from source node at first, and gets the information bits denoted as $\hat{b}_r$. Then, $R$ re-encodes $\hat{b}_r$ by LDPC encoder, and modulates parity encoded bits by BPSK to generate signal denoted as $X_r$. $R$ transmits $X_r$ to $D$. Finally, $D$ performs a concatenated decoding. After receiving the signal $X_r + N_{SD}$ from $S$, $D$ uses RPC decoder1 to demodulate signal, and gets the soft information $\hat{b}_d$. In the next time slot, $D$ receives parity encoding information from relay node, and then $D$ uses $\hat{b}_d$ as side information, performing LDPC decoding. In order to improve performance, both side information and parity information are used in soft information mode.

3. Transmitter

This system can be modeled as the time division relay channel. The total time is divided into three time slots, i.e., $t_1$, $t_2$, and $t_3$, for source node, relay node, and destination node respectively. In this section, we describe the detailed process of the source node and relay node.

3.1. Source Encoding and Broadcasting

The encoding principle of RPC is represented by a bipartite graph shown in Figure 2. Cycles denote bits nodes, and squares denote symbols nodes. A bipartite graph...
denoted as $G = (U, V, E)$. Here $U = \{b_j\}, j = 1, \ldots, N$ is the set of bits nodes, $V = \{u_i\}, i = 1, \ldots, M$ is the set of symbols nodes, and $E$ defines the connections between bit nodes and symbols nodes. A weight corresponding to an edge belongs to the weight set $w = \{w_1, w_2, \ldots, w_L\}$, and expresses energy proportion to symbol node for a corresponds bit node. The connections between bits and symbols can also be described by a $M \times N$ mapping matrix $G$.

Given a bit block $b = (b_1, b_2, \ldots, b_N)$, only the bits with these indices $i_l (l = 1, 2, \ldots, L)$ are sampled by a modulated symbol, so RPC modulated symbol can be written as

$$u_i = \sum_{j=1}^{L} w_j \cdot b_j$$  \hspace{1cm} (3)

where $L$ is the number of non-zeros entities in $i$-th row of $G$, and $b_j$ is signal bits. RPC encodes $b$ into a series of symbols by

$$U = G \cdot b$$  \hspace{1cm} (4)

where $U$ is the generated symbols block for transmission.

RPC encoding and broadcasting takes place in time slot $t_1$. After $S$ is encoded into $U$, each two consecutive symbols $U$ are composed as one modulation signal given by

$$U(2k) + \sqrt{1} \cdot U(2k+1), (k = 0, 1, \ldots, \frac{N}{2} - 1).$$

Finally, $S$ broadcasts $X_s$ to $R$ and $D$.

3.1. Relay Node Decoding and Compress Forward

Relay node performs RPC decoding, LDPC encoding and compressing forward in time slot $t_2$. $R$ first receives signal $Y_{SR}$ from $S$, and $Y_{SR}$ is given by

$$Y_{SR} = X_s + N_{SR}$$  \hspace{1cm} (5)

where $N_{SR}$ is noise term of channel SR, and $N_{SR} \sim N(0, \sigma_{SR}^2)$. Then, $R$ performs RPC decoding, and gets

$$\hat{b}_R = \arg\max_{b \in GF(2^N)} \{P(b | Y_{SR})\},$$  \hspace{1cm} (6)

where $\hat{b}_R$ is the results of hard decision of original RPC decoding algorithm.

After decoding, $R$ uses LDPC parity check matrix $H$ to encode $\hat{b}_R$ again,

$$P_R = H^* \hat{b}_R,$$  \hspace{1cm} (7)

where $P_R$ is the parity bits. Then, $R$ modulates $P_R$ with BPSK to form modulation signal $X_R$, which is transmitted to $D$. Because of only transmitting the parity bits, $R$ acts as compressing and forwarding role.

4. Jointly Decoding at Destination

In this section, we mainly focus on the joint decoding at $D$ by using RPC and LDPC. And, we will analysis the feature of this joint decoding.

4.1. Joint Decoding

The diagram of joint decoding is depicted as Figure 3. From $y-axis$, joint decoding at $D$ is divided into two stages, one is RPC decoding, and the other is LDPC decoding. We use the original RPC decoding algorithm in joint decoding algorithm, but modify the hard decision part to output soft information as one part of input of LDPC decoding. The soft information correspond the information bits nodes from $b_i$ to $b_{N_1}$, and can be regarded as the side information in DSC. In addition to information bits, there exist parity bits in $x-axis$, i.e., bits node from $b_{N_s+1}$ to $b_{N_2}$, which corresponds the channel prior information from relay node, just like compressing information in DSC.

$D$ performs decoding in two consecutive time slots, i.e., $t_2$ and $t_3$. In time slot $t_2$, $D$ receives the signal denoted as $Y_{SD}$ from source node $S$, and $Y_{SD}$ can be expressed as

$$Y_{SD} = X_s + N_{SD}$$  \hspace{1cm} (8)

Then, $D$ performs RPC decoding algorithms to get soft informations denoted as $\hat{b}_D$,

$$\hat{b}_D = \arg\max_{b \in GF(2^N)} \{P(b | Y_{SD})\}$$  \hspace{1cm} (9)

Here, we modify the hard decision part of RPC decoding algorithm proposed in [4]. Modified RPC decoding algorithm does not estimate the hard decision of input binary digit, but outputs the probability of corresponding bits.
In time slot $t_1$, after D received signal from relay node, D performs LDPC decoding. D receives the signal $Y_{RD}$ from R, and $Y_{RD}$ can be presented by

$$Y_{RD} = X_R + N_{SD}$$ (10)

Next, D assemble soft information from RPC decoder and soft parity information from relay node together, we get input data as following,

$$Q^J_{j,i}(0) = \left[ \begin{array}{c} 0 \\ b_D, Y_0 \end{array} \right]$$ and $$Q^A_{j,i}(0) = \left[ \begin{array}{c} 1 \\ b_D, Y_1 \end{array} \right]$$ (11)

where

$$Y_i(i) = \frac{1}{1 + e^{-2Y_{DD}(i)\sigma_{DD}^2}}, \text{ and } Y_0 = 1 - Y_1$$ (12)

4.2. Analysis

The scheme proposed in this paper has two important features. The first is that it realizes the rate adaptation at receiver. Due to using RPC decoding at relay node and destination node, our system will automatically decode the signal from source node according to the channel quality. As long as source node does not receive the ACK from destination node, it will keep broadcasting more and more modulation symbols to relay and destination node. With the increase of receiving symbols, the probability of RPC decoding success will improved. Otherwise, source node will send next data frame. With the help of LDPC encoding at relay node, the joint decoding performance in destination node is significantly improved. Thus, the number of demodulation symbols for RPC decoding can be dramatically reduced. In another words, joint decoding can decode successfully with much less symbols from source node with the help of relay. The second one is that joint decoding at destination node has two parts input, i.e., parity information from relay node and soft information from RPC encoder2. The joint decoding performance will be improved with the improvement of reliability of input data. Obviously, the reliability of soft information and parity information are improved with the help of relay node.

5. Simulation Results

In this section, we implement the decoding algorithm of RPC and LDPC algorithm, and use them to construct a relay system discussed in Section II. In our experiment, we select the SNR in SD link from 5dB to 30dB, and think as usual the SNR in SR link and RD link are all better than SD link. Here we consider a special case in relay model where the distance proportion of relay node to source node, relay node to destination node and source node to destination node ($d_{SR}:d_{RD}:d_{SP}$) is 7:3:1 and 3:7:1. In addition we experiment the bits block size is $N = 400$ and the numbers of parity bits generated in relay node is 100, 50, 25 and 10 corresponding to the different LDPC coding rate 0.8, 0.89, 0.94 and 0.99 respectively. Finally, we use goodput [18] and BER as evaluation metrics to evaluate the performance of our scheme. No matter what kinds of evaluation metric, we all assume that ACK is immediately available in our simulation.

Figure 4 compares the BER performance in different schemes. Here “S-D” is the original RPC algorithm, “Relay$_{R=0.8}$”, “Relay$_{R=0.89}$”, “Relay$_{R=0.94}$” and “Relay$_{R=0.99}$” is our scheme with different LDPC coding rate in relay node. From Figure 4, we can see that all these relay schemes are better than “S-D”. And with the LDPC coding rate dropping the performance of our scheme is better and better. For example, as shown in Figure 4(a), when $d_{SR}:d_{RD}=3:7$ and BER equal 10-5, our relay schemes have performance improved about 4dB, 3.1dB, 2.2dB and 1.6dB more than “S-D”. In addition, we can find that there are the similar performances when $d_{SR}:d_{RD}=7:3$ as shown in Figure 4(b).

The main reason is that it is usually thought that the communication condition in SR link and RD link are all better than SD link, so the RPC decoding performance in relay node is better than destination node, which makes the correctness of RPC decoding in relay node all improved and the reliability of the parity bits similar in destination too. Thus, with the help of relay node, the whole system performance is less relation with the relay node’s location when the LDPC rate in relay node is the same.

We also evaluate our scheme by good put metric in the actual wireless scenario. Here we set the bits block size is $N = 400$ and the sending increasing step in source node is 10 modulation signals, the SNR range from 5dB to 30dB and $d_{SR}:d_{RD}=7:3$. In order to test the performance of our scheme, we also consider the LDPC rate with 0.8 and 0.94. Figure 5 shows the result where “S-D” is the original system without relay, and “Relay$_{R=0.8}$” and “Relay$_{R=0.94}$” are our scheme. We can observe that our different schemes are all higher than “S-D” at least 0.38 bit/s/Hz and 0.28 bit/s/Hz when SNR from 5dB to 25dB. But all the throughputs are almost the same value 6.6 bit/s/Hz when SNR more than 25dB.Obviously, the reason we can achieve higher throughput is the relay’s help. In addition, we also can observe that with the increasing of the LDPC rate, the goodput became higher and higher. The main reason is that the LDPC rate higher can provide more protection to information bits.

6. Conclusions

This paper investigates the scheme for parallel concatenation of LDPC and RPC. Though RPC perform the functionality of coding and modulation simultaneously, its
coding performance is not good enough. As we expect, the concatenation of LDPC and RPC brings significant gain, e.g., coding gain 4 dB, 3.1 dB, 2.2 dB and 1.6 dB for coding rate 0.8, 0.89, 0.94 and 0.99 respectively.

On the other hand, parallel concatenation structure fits the 3-node relay system well. In order to reduce overhead, we make use of distributed source code concept here, and relay node only sends parity code bits. The destination node uses the direct-link signal as side information, and relay-link signal to perform joint decoding. This scheme further improves rate adaptation feature of RPC. The efficient improvement of 0.3 bit/s/Hz spectrum is obtained. Next step, we plan to investigate the joint code design of RPC and LDPC to optimize performance further.

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